CLAIMS

What is claimed is:

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- 1. A resonant tunneling diode comprising:
 - (a) a substrate of (100) GaAs;
- (b) a multilayer resonant tunneling diode structure grown on the (100) GaAs substrate, the resonant tunneling diode structure comprising a quantum well layer of low band-gap material between barrier layers of AlGaAs, and wherein the material of the quantum well layer is selected such that the second energy level of the quantum well layer is at or slightly above the conduction band edge in GaAs, the quantum well layer grown to be a strained layer with smooth interfaces with the barrier layers.
- 2. The resonant tunneling diode device of Claim 1 wherein the quantum well layer comprises InGaAs.
- 3. The resonant tunneling diode device of Claim 2 wherein the barrier layers are composed of $Al_{0.8}Ga_{0.2}As$, and the quantum well layer is formed of $In_{0.3}Ga_{0.7}As$.
- 4. The resonant tunneling diode device of Claim 3 wherein the barrier layers are about 14Å thick and the quantum well layer is about 57Å thick.
- 5. The resonant tunneling diode device of Claim 2 including a spacer layer of intrinsic GaAs adjacent to each barrier layer.
- 6. The resonant tunneling diode device of Claim 5 further including a layer of n-doped GaAs adjacent to each GaAs intrinsic layer.
- 7. The resonant tunneling diode device of Claim 6 wherein the substrate is doped GaAs and the resonant

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tunneling diode structure is isolated on a mesa with a doped GaAs layer at the top of the mesa and further including an insulating layer over the exposed surfaces of the substrate and the mesa and a contact formed through the insulating layer to the doped GaAs layer, and a contact on the doped GaAs substrate opposite the mesa.

- 8. The resonant tunneling diode device of Claim 1 wherein the resonant tunneling diode structure is grown on a nominally exact (100) +/- 0.5° GaAs substrate.
- 9. The resonant tunneling diode device of Claim 1 wherein the resonant tunneling diode structure is grown on a nominally exact (100) +/- 0.1° GaAs substrate.
- 10. The resonant tunneling diode device of Claim 1 wherein the resonant tunneling diode structure is grown on the GaAs substrate by metal organic chemical vapor deposition.
 - 11. A resonant tunneling diode comprising:
 - (a) a substrate of (100) GaAs;
- (b) a multilayer resonant tunneling diode structure grown on the (100) GaAs substrate, the resonant tunneling diode structure comprising a quantum well layer of InGaAs between barrier layers of AlGaAs, a spacer layer of intrinsic GaAs adjacent to each barrier layer, and a layer of n-doped GaAs adjacent to each GaAs intrinsic layer, wherein the second energy level of the quantum well layer is at or slightly above the conduction band edge in GaAs, the quantum well layer grown by metal organic chemical vapor deposition to be a strained layer with smooth interfaces with the barrier layers.
- 12. The resonant tunneling diode device of Claim
 11 wherein the barrier layers are composed of Al_{0.8}Ga_{0.2}As, and

 the quantum well layer is formed of In_{0.3}Ga_{0.7}As.

13. The resonant tunneling diode device of Claim
2 12 wherein the barrier layers are about 14Å thick and the
3 quantum well layer is about 57Å thick.

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- 14. The resonant tunneling diode device of Claim 11 wherein the substrate is doped GaAs and the resonant tunneling diode structure is isolated on a mesa with a doped GaAs layer at the top of the mesa and further including an insulating layer over the exposed surfaces of the substrate and the mesa and a contact formed through the insulating layer to the doped GaAs layer and a contact on the doped GaAs substrate opposite the mesa.
- 15. The resonant tunneling diode device of Claim 11 wherein the resonant tunneling diode structure is grown on a nominally exact (100) +/- 0.5° GaAs substrate.
- 16. The resonant tunneling diode device of Claim 11 wherein the resonant tunneling diode structure is grown on a nominally exact (100) +/- 0.1° GaAs doped substrate.
- 17. A method of making a resonant tunneling diode comprising the steps of:
- (a) providing a substrate of nominally exact(100) GaAs;
- (b) growing on the substrate by metal organic chemical vapor deposition successive layers comprising at least a barrier layer of AlGaAs, a strained quantum well layer of low band-gap material, and a barrier layer of AlGaAs, the quantum well layer grown with smooth interfaces with the barrier layers with the second energy level of the quantum well layer at or slightly above the conduction band edge in GaAs.
- 18. The method of Claim 17 wherein the quantum well layer material is InGaAs.

1	19. The method of Claim 15 wherein the barrier
2	layers are formed to be composed of ${\tt A1_{0.8}Ga_{0.2}As}$, and the
3	quantum well layer is formed to be composed of $In_{0.3}Ga_{0.7}As$.
1	20. The method of Claim 17 further including
2	growing by metal organic chemical vapor deposition a spacer
3	layer of intrinsic GaAs adjacent to each barrier layer.
1	21. The method of Claim 20 further including
2	growing by metal organic chemical vapor deposition a layer
3	of n-doped GaAs adjacent to each GaAs intrinsic layer.
= 1	22. The method of Claim 21 wherein the substate
₫ 2	is doped GaAs and including isolating the resonant
2	tunneling diode structure on a mesa with a doped GaAs layer
4	at the top of the mesa and further including forming an
5	insulating layer over the exposed surfaces of the substrate
<u> </u>	and the mesa and forming a contact through the insulating
7	layer to the doped GaAs layer and forming a contact on the
≟ 8	doped GaAs substrate opposite the mesa.
1	23. The method of Claim 17 wherein the substrate
2	is nominally exact (100) $\pm/-$ 0.5° GaAs.
1	24. The method of Claim 17 wherein the substrate

is nominally exact (100) +/- 0.1° GaAs.

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